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PATENT

## **Technology Center 2100**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Suaya et al.

Application No. 09/385,666

Filed: August 26, 1999

For: CAPACITANCE MEASUREMENTS FOR

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AN INTEGRATED CIRCUIT

Examiner: Thai Q. Phan

Date: February 10, 2003

Art Unit: 2123

## **CERTIFICATE OF MAILING**

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service on February 10, 2003, as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.

Attorney for Applicant

## INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. § 1.97(c)

COMMISSIONER FOR PATENTS WASHINGTON, DC 20231

Listed on the accompanying form PTO-1449 and enclosed herewith is an English-language document. Applicants respectfully request that this document be listed as a reference cited on the issued patent. This Information Disclosure Statement is being mailed before Applicants received a final action, a notice of allowance, or an action that otherwise closes prosecution in the referenced application.

Submitted herewith is a check for \$180.00 as required by 37 C.F.R. § 1.17(p) for filing this Information Disclosure Statement in compliance with 1.97(c).

Please charge any additional fees which may be required in connection with filing this Information Disclosure Statement, or credit any overpayment, to Deposit Account No. 02-4550. A duplicate copy of this sheet is enclosed.

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